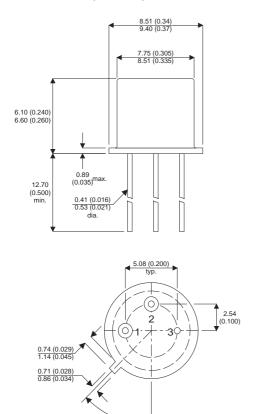


2N5010

MECHANICAL DATA Dimensions in mm (inches)



SILICON EPITAXIAL NPN TRANSISTOR

FEATURES

General purpose power transistor for switching and linear applications in a hermetic TO-39 package.

TO-39 (TO-205AD) PACKAGE

PIN 1 - Emitter PIN 2 - Base PIN 3 - Collector

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise stated)

V _{CBO}	Collector – Base Voltage	500V
V _{CER}	Collector – Emitter Voltage $R = 10\Omega$	500V
V _{EBO}	Emitter – Base Reverse Voltage	5V
I _C	Continuous Collector Current	0.5A
P _{TOT}	Total Device Dissipation $T_{\rm C} = 25^{\circ}{\rm C}$	2W
T _{J.} T _{STG}	Maximum Storage and Junction Temperature Range	200°C
$R_{ extsf{ heta}JC}$	Thermal Impedance Junction To Case	50°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise stated)

Parameter		Test Conditions		Min.	Тур.	Max.	Unit
I _{CBO}	Collector Base Leakage Current	V _{CB} = 500V				0.006	mA
h _{FE}	D.C Current Gain	V _{CE} = 10V	I _C =0.025A	30		180	—
ft	Transition Frequency				20		MHz

Semelab PIc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.